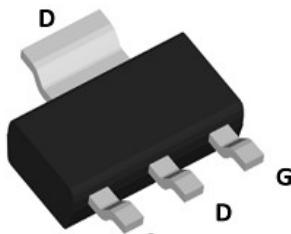
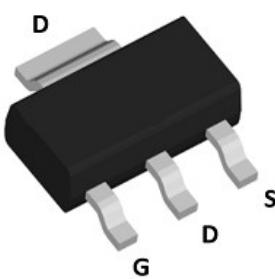
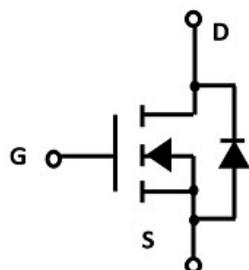


## N-Channel Enhancement Mode Field Effect Transistor



SOT-223



### Product Summary

- $V_{DS}$  60V
- $I_D$  5.0A
- $R_{DS(ON)}$  (at  $V_{GS}=10V$ ) <100 mohm
- $R_{DS(ON)}$  (at  $V_{GS}=4.5V$ ) <120 mohm

### General Description

- Trench Power MV MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low  $R_{DS(ON)}$

### Applications

- DC-DC Converters
- Power management functions

### ■ Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	$V_{DS}$	60	V
Gate-source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current	$I_D$	5.0	A
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	12	A
Total Power Dissipation @ $T_c=25^\circ\text{C}$	$P_D$	1.2	W
Thermal Resistance Junction-to-Ambient <sup>B</sup>	$R_{\theta JA}$	105	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~+150	$^\circ\text{C}$

■ Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	60			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$			1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS1}}$	$V_{\text{GS}}= \pm 20\text{V}, V_{\text{DS}}=0\text{V}$			$\pm 100$	nA
	$I_{\text{GSS2}}$	$V_{\text{GS}}= \pm 10\text{V}, V_{\text{DS}}=0\text{V}$			$\pm 50$	nA
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.7	2.5	V
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}= 10\text{V}, I_{\text{D}}=3\text{A}$		65	100	$\text{m}\Omega$
		$V_{\text{GS}}= 4.5\text{V}, I_{\text{D}}=1.5\text{A}$		78	120	
Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{S}}=3.0\text{A}, V_{\text{GS}}=0\text{V}$		0.8	1.2	V
Maximum Body-Diode Continuous Current	$I_{\text{S}}$				3.0	A
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		330		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			90		
Reverse Transfer Capacitance	$C_{\text{rss}}$			17		
<b>Switching Parameters</b>						
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=30\text{V}, I_{\text{D}}=3.0\text{A}$		5.1		$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$			1.3		
Gate-Drain Charge	$Q_{\text{gd}}$			1.7		
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=10\text{V}, V_{\text{DD}}=30\text{V}, I_{\text{D}}=1.5\text{A}, R_{\text{L}}=1\Omega, R_{\text{GEN}}=3\Omega$		13		$\text{ns}$
Turn-on Rise Time	$t_{\text{r}}$			51		
Turn-off Delay Time	$t_{\text{D(off)}}$			19		
Turn-off fall Time	$t_{\text{f}}$			12		

A. Pulse Test: Pulse Width  $\leq 300\text{us}$ , Duty cycle  $\leq 2\%$ .

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

## ■ Typical Performance Characteristics

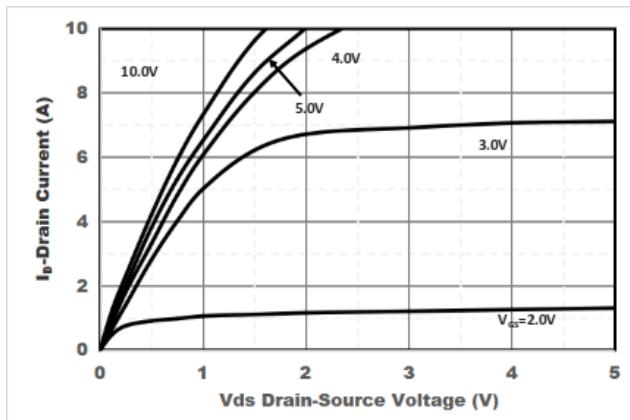


Figure1. Output Characteristics

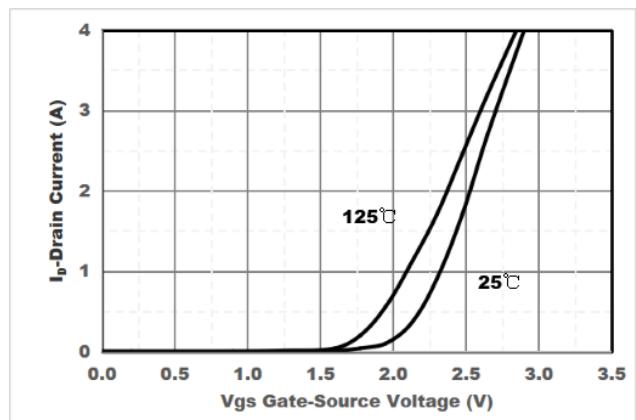


Figure2. Transfer Characteristics

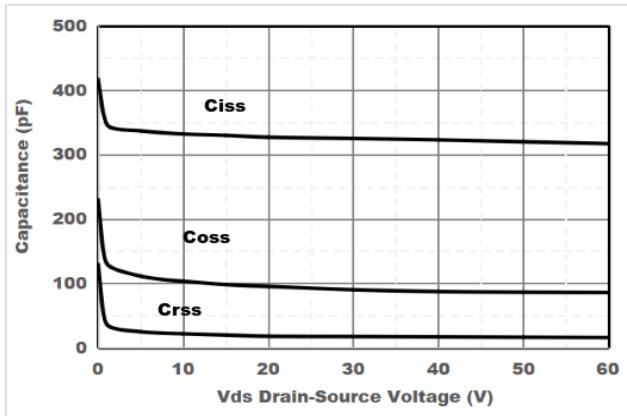


Figure3. Capacitance Characteristics

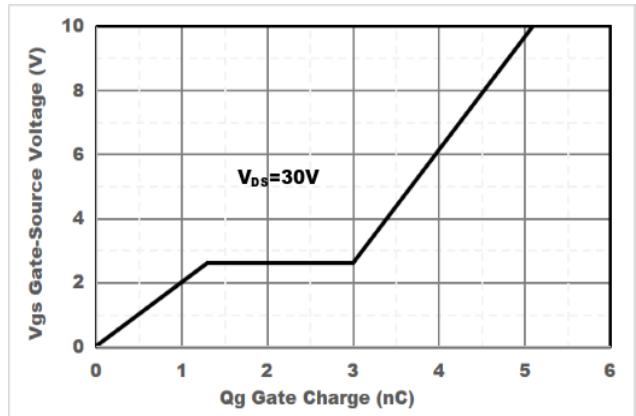


Figure4. Gate Charge

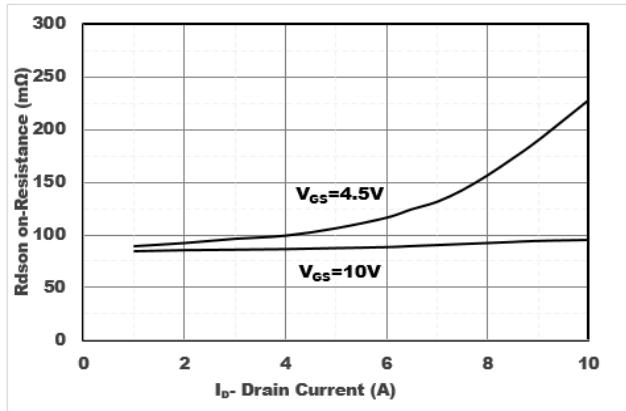


Figure5. Drain-Source on Resistance

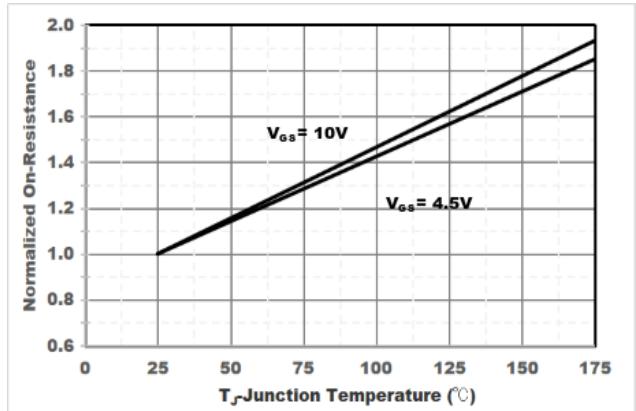


Figure6. Drain-Source on Resistance

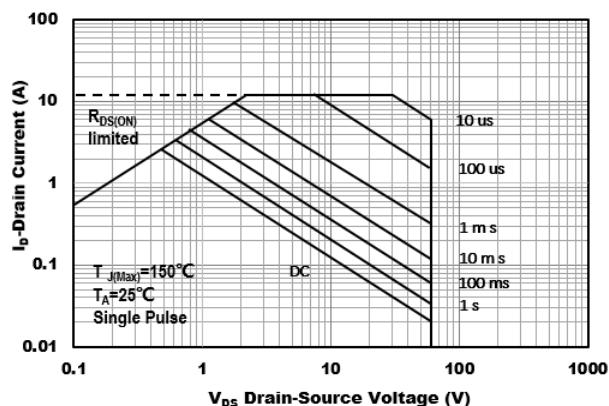


Figure7. Safe Operation Area

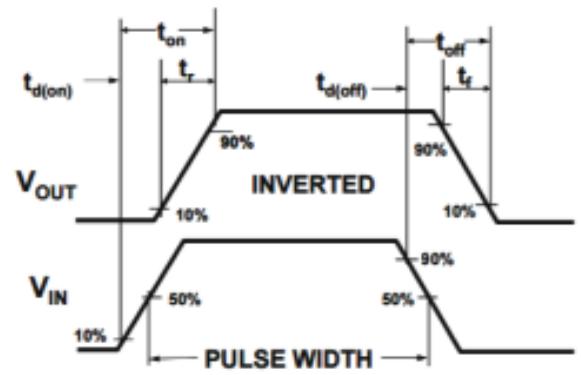
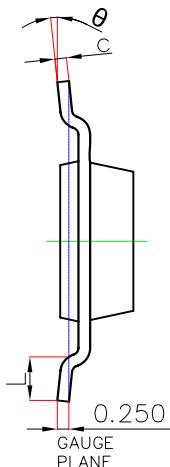
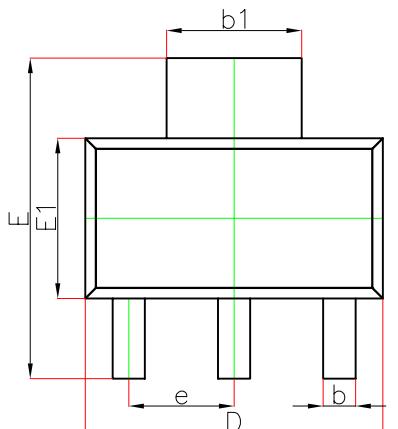


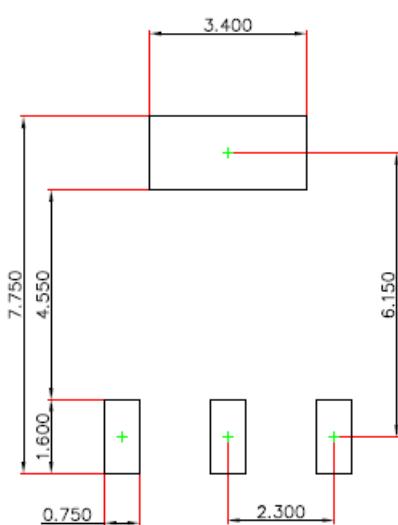
Figure8. Switching wave

## ■ SOT-223 Package information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	—	1.800	—	0.071
A1	0.020	0.100	0.001	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.840	0.026	0.033
b1	2.900	3.100	0.114	0.122
c	0.230	0.350	0.009	0.014
D	6.300	6.700	0.248	0.264
E	6.700	7.300	0.264	0.287
E1	3.300	3.700	0.130	0.146
e	2.300(BSC)		0.091(BSC)	
L	0.750	—	0.030	—
$\theta$	0°	10°	0°	10°

## ■ SOT-223 Suggested Pad Layout



### Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.050\text{mm}$ .
3. The pad layout is for reference purposes only.